

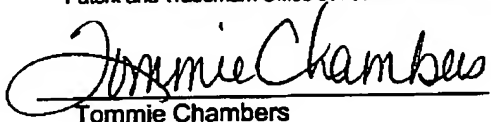
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From: W. Daniel Swayze, Jr.
Texas Instruments Incorporated
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATION OF FACSIMILE TRANSMISSION	
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NAME OF INVENTOR(S): Barnett	RECEIPT DATE & SERIAL NO.: Serial No.: 10/696,827 Filing Date: 10/30/2003
TITLE OF INVENTION: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS AND FOR SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP CURRENT BIAS ARCHITECTURE	
TI FILE NO.: TI-36061	DEPOSIT ACCT. NO.: 20-0668
FAXED: <u>3-30-05</u> DUE: 04/04/2005 ATTY/SEC'Y: WDS/mc	

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Barnett, et al.

Docket No: TI-36061

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Serial No: 10/696,827

Examiner: Wells, Kenneth B.

MAR 30 2005

Filed: 10/30/2003

Art Unit: 2816

For: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS AND FOR
SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP CURRENT BIAS ARCHITECTURE

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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Tommie Chambers
Tommie Chambers 3-30-05

Applicant hereby appeals to the Board of Appeals from the decision mailed 01/04/2005, of the Primary Examiner finally rejecting Claims 1-7.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection
☐ was granted on _____.
☐ is requested for ____ month.
2. ☒ A timely response to the final rejection has been filed, as provided in
841 O.G. 1411.
3. ☒ Fee \$500.00:
☐ Not required (Fee paid in prior appeal)
☒ The Commissioner of Patents is hereby authorized to charge any
fees which may be required or credit any overpayment to the deposit
account of Texas Instruments Incorporated, Account No. 20-0668.
This form is submitted in duplicate.

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